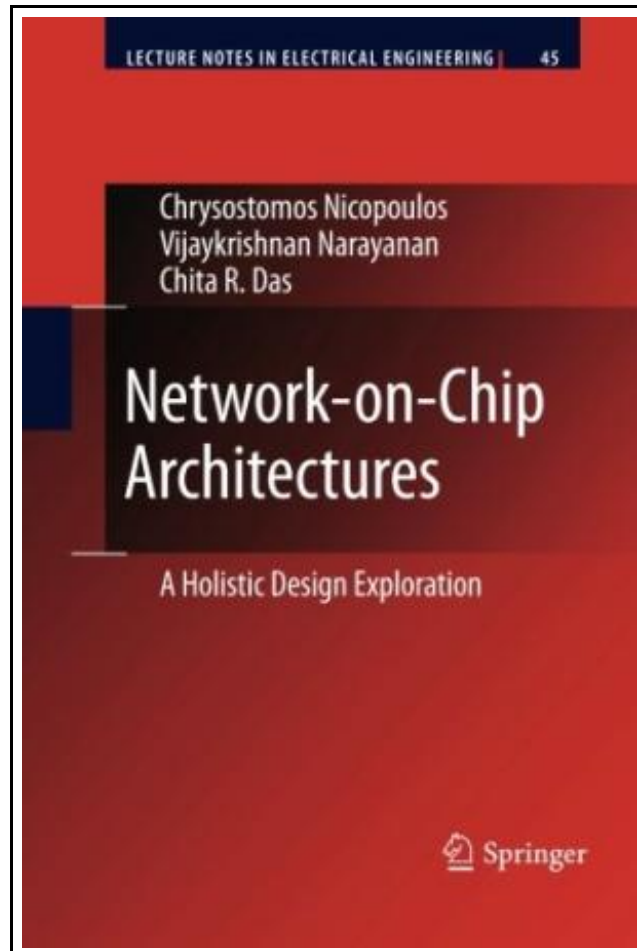


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NETWORK-ON-CHIP ARCHITECTURES: A HOLISTIC DESIGN EXPLORATION



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